

10/15/2013			
Time	Session	Program	Venue
18:30~21:00		Welcome Reception	1F Conference Room Lobby

First Day 10/16/2013			
Time	Session	Program	Venue
08:30~08:40		Opening	1F Conference Room
08:40-09:30	Keynote Session I	Topic: Time-Reversal Paradigm for Green Wireless Communications Speaker: Prof. Ray Liu (Professor of University of Maryland, USA. President of IEEE SPS)	1F Conference Room
09:30-09:50		Coffee Break	1F Lobby
09:50-11:10	Lecture Session I	Topic: Optimization of Wireless Communication Signal Processing Systems Session Chair: Cyrille Chavet, Lab-STICC, France	1F Conference Room
11:10-12:30	Lecture Session II	Topic: Signal processing for communications Session Chair: Chih-Peng Fan, National Chung Hsing University, Taiwan	1F Conference Room
12:30-13:40		Lunch Time	2F Restaurant
13:40-15:00	Lecture Session III	Topic: Signal processing for biomedical and image applications Session Chair:	1F Conference Room
15:00-16:00	Poster Session I	Topic: Communication Signal Processing Systems & Applications Session Chairs: Mladen Berekovic, TU Braunschweig, Germany; Min Li, Imec, leuven, Belgium	1F Lobby
16:00-17:40	Lecture Session IV (note 5 papers)	Topic: VLSI/FPGA based Design and Implementation of Signal Processing Systems Session Chair: Lih-Jen Kau, National Taipei University of Technology, Taiwan	1F Conference Room

Second Day 10/17/2013			
Time	Session	Program	Venue
08:30-09:20	Keynote Session II	Topic: Where can Signal Processing and Machine Learning Algorithms help Automate Diagnostic Procedures in Medical Imaging? Speaker: Prof. Matt O'Donnell (Professor and Dean of College of Engineering, University of Washington, USA)	1F Conference Room
09:20-09:40		Coffee Break	1F Lobby



09:40-11:00	Lecture Session V	Topic: Multimedia System Design Flow and Applications Session Chair: Chris Lee, National Cheng Kung University, Taiwan	1F Conference Room
11:00-12:20	Lecture Session VI	Topic: Design Methodology and tools for Signal Processing Systems Session Chair: Emmanuel Boutillon, Lab-STICC, France	1F Conference Room
12:20-13:40	Lunch Time		2F Restaurant
12:20-13:40	DISPS TC Meeting		Wistaria Tea House
13:40-15:00	Lecture Session VII	Topic: Software based Design and Implementation of Signal Processing Systems Session Chair: Jarmo Takala, Tampere University of Technology, Finland	1F Conference Room
15:00-16:00	Poster Session II	Topic: Multimedia/Biomedical Signal Processing Application Systems Session Yu-Cheng Fan, Nation Taipei University of Technology, Taiwan	1F Lobby
16:00-17:30	Panel Discussion	Topic: What are the design challenges of Chip Multiprocessor (CMP) and Smart platform, the new frontier for Implementations of Signal Processing Systems? Moderator: Yen-Kuang Chen, Intel, USA	1F Conference Room
17:30-17:50	Bus transportation to Banquet Site		Hotel Entrance
18:30-21:30	Banquet		The Red House
Third Day 10/18/2013			
Time	Session	Program	Venue
08:30-09:15	Keynote Session III	Topic: Self-Aware Communication and Signal Processing Systems: Real-Time Adaptation to Power, Error Resilience and Workload Demands Speaker: Prof. Abhijit Chatterjee (Professor of School of Electrical and Computer Engineering, Georgia Tech, USA)	1F Conference Room
09:15-09:30	Coffee Break		1F Lobby
09:30-10:50	Lecture Session VIII	Topic: Signal Processing to Rescue Moore's Law (I) Session Chair: Lei Wang, University of Connecticut, United States	1F Conference Room
10:50-12:10	Lecture Session IX	Topic: Signal Processing to Rescue Moore's Law (II) Session Chair: Rami Abdallah, Intel, United States	1F Conference Room
12:10-13:10	Poster Session III	Topic: Optimization of Signal Processing Algorithms and Architectures Session Chair: Bo-Cheng Lai, National Chiao Tung University, Taiwan	1F Lobby
13:10-13:20	Closing (served with lunch boxes)		1F Conference Room